

(12) UK Patent Application (19) GB (11) 2 372 162 (13) A

(43) Date of A Publication 14.08.2002

(21) Application No 0125125.5

(22) Date of Filing 19.10.2001

(30) Priority Data

(31) 00262139 (32) 25.10.2000 (33) GB

(71) Applicant(s)

Zarlink Semiconductor Limited
(Incorporated in the United Kingdom)
Cheney Manor, SWINDON, Wilts, SN2 2QW,
United Kingdom

(72) Inventor(s)

Nicholas Paul Cowley

(74) Agent and/or Address for Service

Marks & Clerk
4220 Nash Court, Oxford Business Park South,
OXFORD, OX4 2RU, United Kingdom

(51) INT CL⁷

H03D 7/00

(52) UK CL (Edition T)

H3R RMX

U1S S2207

(56) Documents Cited

GB 2223900 A

US 4162452 A

US 4162451 A

(58) Field of Search

UK CL (Edition T) H3R RMX

INT CL⁷ H03D 7/00 7/06 7/08 7/10 7/12 7/16

Online:WPI, EPODOC, JAPIO

(54) Abstract Title

Modem tuner

(57) A modem tuner of ZIF or NZIF type has a mixer (8) which receives first and second inputs in different frequency ranges via a multiplexer (5) from different sources such as cable (2) and satellite (1). A local oscillator (9) supplies signals via a bandswitch (25) which switches the local oscillator frequency range in synchronism with the selection of inputs by the multiplexer (5). The mixer (8) converts the selected incoming data stream to zero or near-zero intermediate frequency and this signal is filtered exclusively by a variable bandwidth low-pass filter (26) before being converted to the digital domain and demodulated (14).

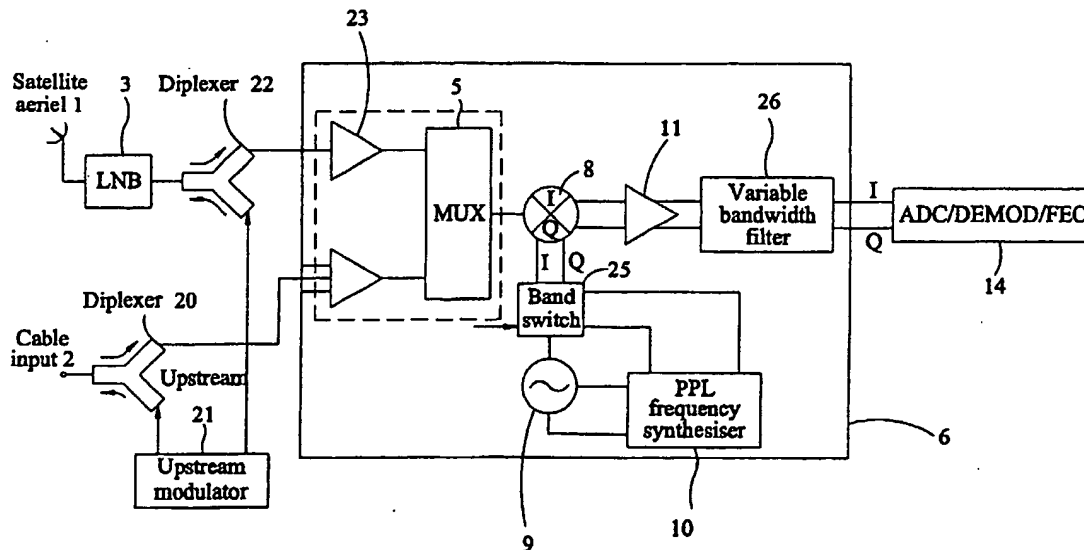


FIG 3

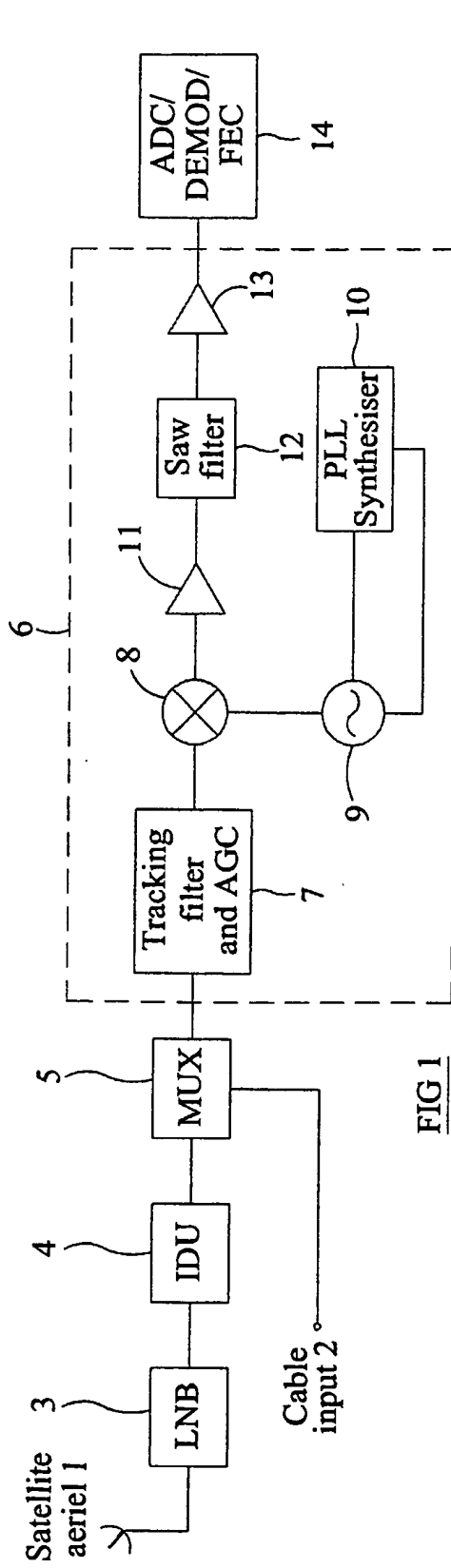


FIG 1

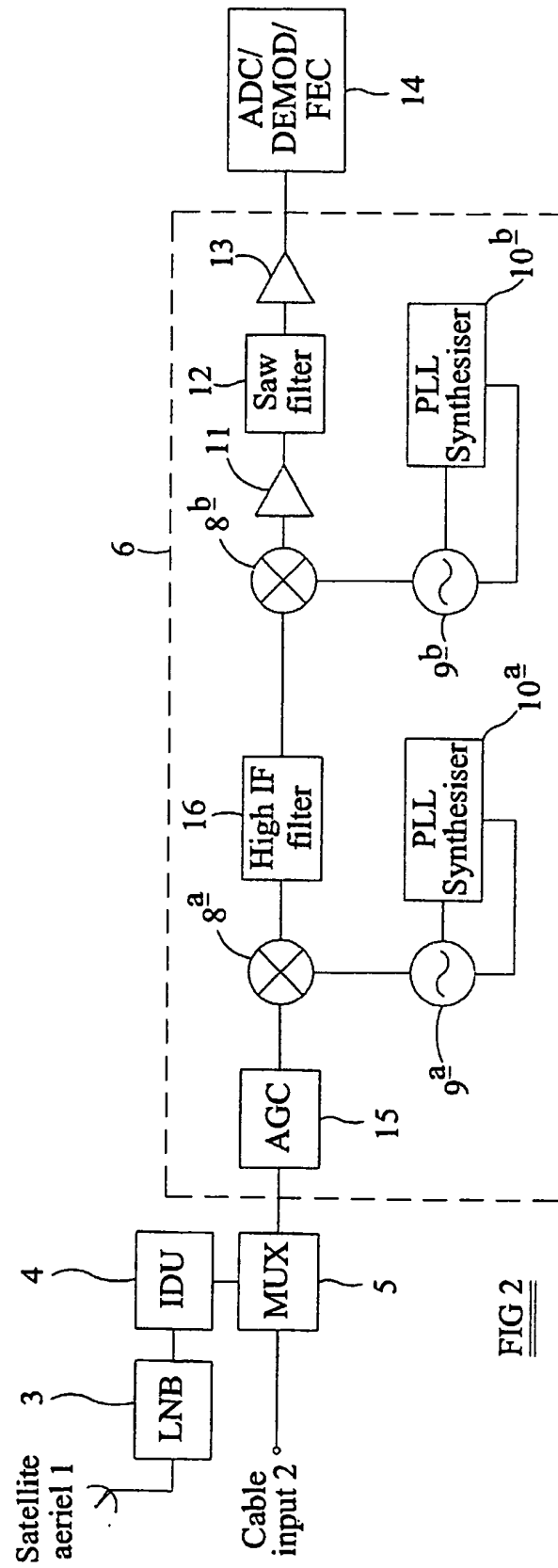


FIG 2

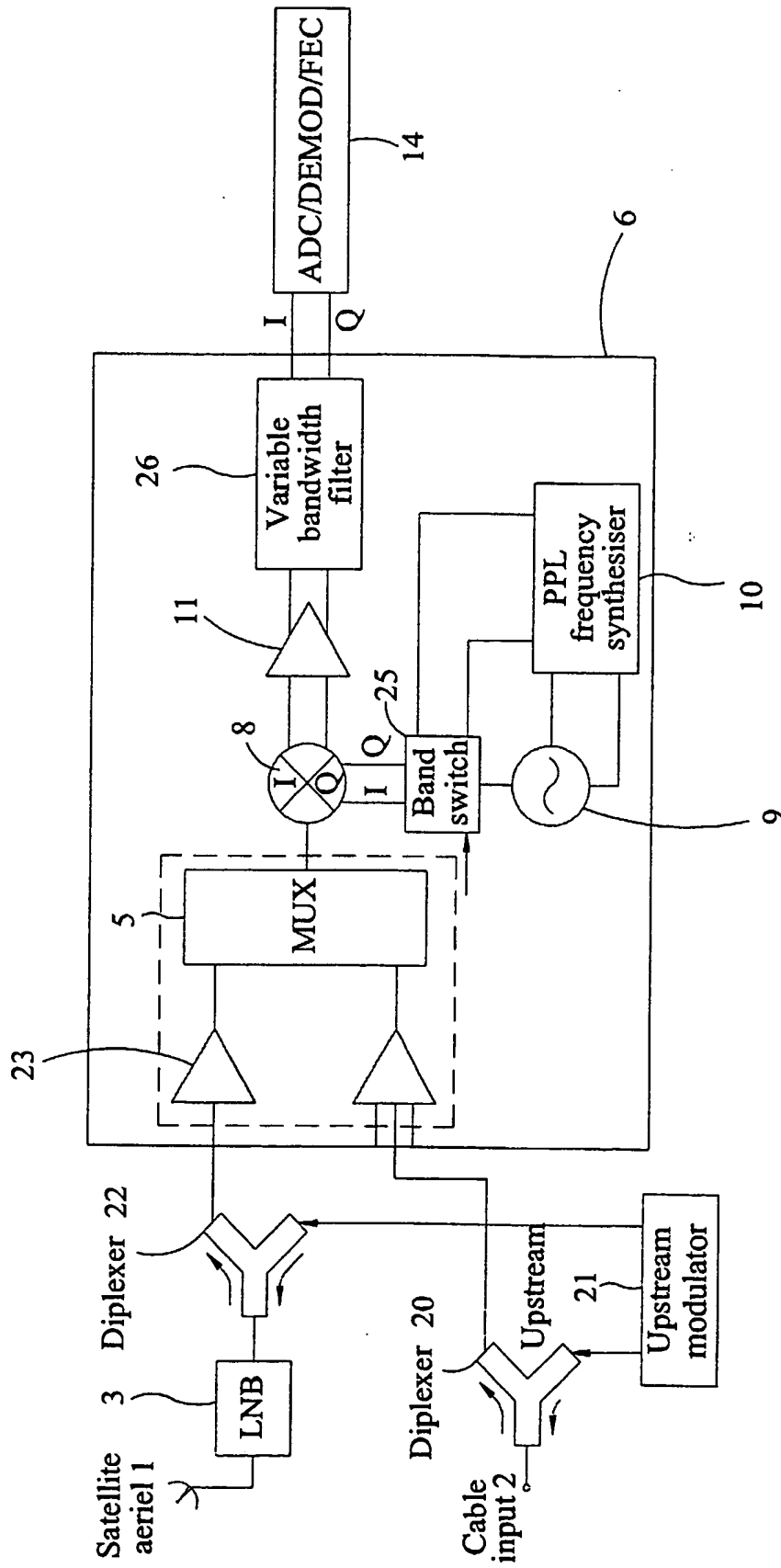


FIG 3

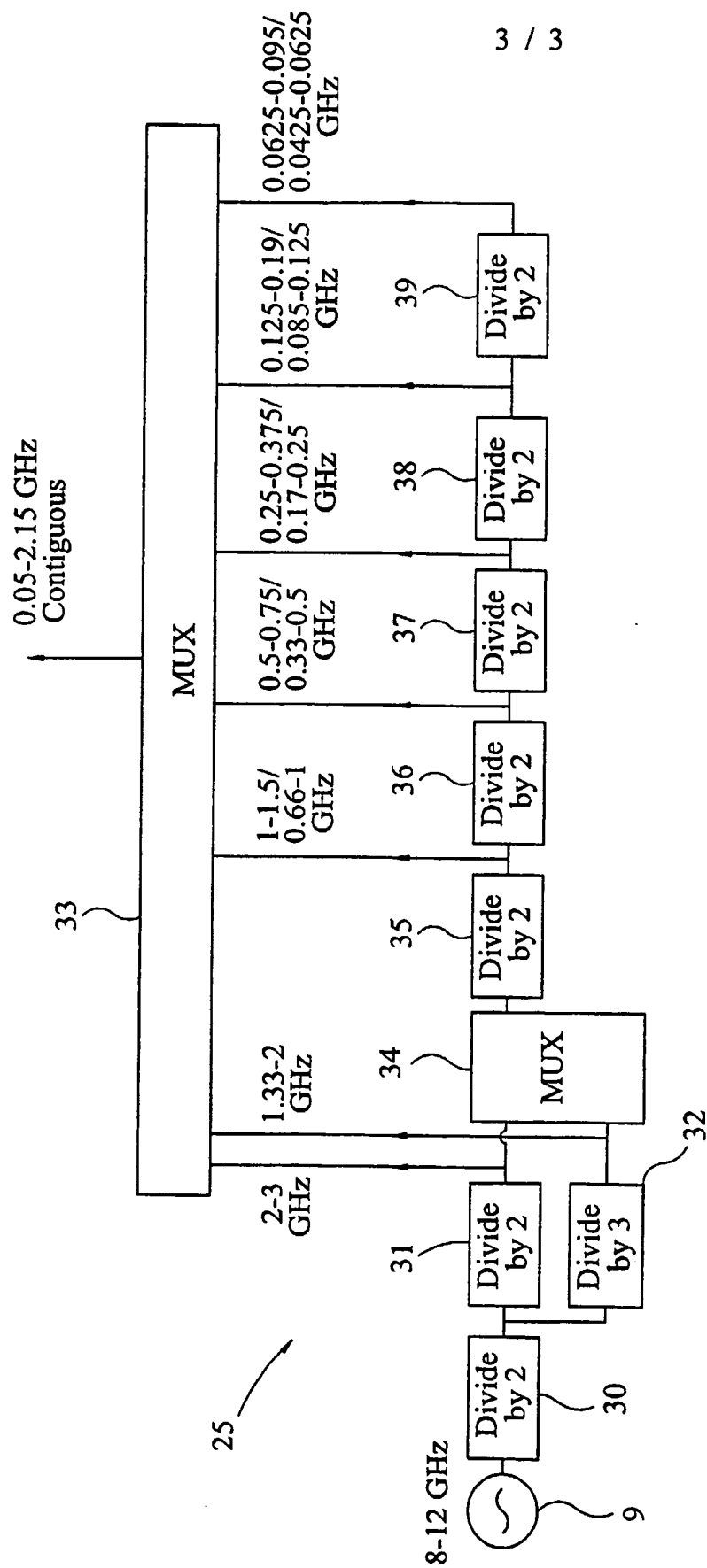


FIG 4

MODEM TUNER

The present invention relates to a modem tuner. Such a tuner may be used for multi-mode modem applications, for example including cable systems, multi-point multi-channel distribution systems (MMDS) and local multi-point distribution service (LMDS).

Known types of modem tuners are based closely on cable standards, for example in terms of channel spacing, data rates and modulation schemes. Figure 1 of the accompanying drawings illustrates a typical known modem tuner of the single conversion type for receiving signals selectively from a satellite aerial 1 or a cable input 2. The satellite aerial is connected to a low noise block (LNB) 3, which receives signals, for example, in a band between 3GHz and 22GHz. The LNB 3 comprises a frequency changer which converts these frequencies into the L band between 900MHz and 2.2GHz. These signals are supplied to an in-door unit (IDU) 4, which converts the signals to be same frequency band as used by a cable distribution system to which the cable input 2 is connected, for example 50 to 900MHz. The output of the IDU 4 and the cable input 2 are connected to respective inputs of a multiplexer (MUX) 5, which selects which of the signal sources is connected to the input of a tuner 6.

The tuner input is connected to a tracking filter and automatic gain control (AGC) circuit 7. The output of the circuit 7 is connected to the signal input of a mixer 8, which receives a local oscillator signal from a local oscillator 9 controlled by a phase locked loop (PLL) synthesiser 10. The tuner 6 is of the single conversion type and the mixer 8 converts the selected channel, which may typically have a bandwidth of 8MHz, so that it is centred on the intermediate frequency, which is typically 44MHz. The intermediate frequency signal is supplied via a buffer 11 to a surface acoustic wave (SAW) intermediate frequency bandpass filter 12 which, for the example mentioned above, has a passband of 8MHz centred on a centre frequency of 44MHz. The output of the filter 12 is supplied via a buffer 13 to a circuit 14, which performs analogue/digital conversion, demodulation and forward error correction.

Figure 2 of the accompanying drawings illustrates an alternative type of tuner of the double conversion type. Like reference numerals refer to like parts in Figures 1 and 2 and such like parts will not be described again.

The tuner 6 of Figure 2 comprises an input AGC stage 15 whose output is connected to a first frequency changer comprising a mixer 8a connected to a local oscillator 9a controlled by a PLL synthesiser 10a. The first frequency changer converts the incoming signal selected by the multiplexer 5 to a relatively high intermediate frequency, for example 1.2GHz. This is filtered by a high intermediate frequency (IF) bandpass filter 16 and supplied to a second frequency changer comprising a mixer 8b connected to a local oscillator 9b controlled by a PLL synthesiser 10b. The second frequency changer converts the high IF signal to a second low intermediate frequency which is typically 44MHz. The second IF signal is then buffered and filtered before being supplied to the circuit 14 for conversion to and processing in the digital domain.

Known arrangements of the type shown in Figures 1 and 2 suffer from various disadvantages. For example, because the signals from the satellite aerial 1 are converted to the same frequency range as the signals from the cable distribution system, interactions may occur between signals from the two sources. For example, if the tuner 6 is set to receive a desired channel from one of the sources and a channel at the same frequency is present from the other source, interactions may occur between the two data streams carried by the signals so that the desired data stream suffers interference.

There are proposed new standards for transmission systems which require that channel bandwidths be variable and increased. Also, different data rates and types of modulation are being proposed in such standards. Existing data rates are typically in the range of 100kbyte/sec to 45Mbyte/sec and there are presently proposals for data rates up to 200Mbytes/sec. Modem tuners of the type shown in Figures 1 and 2 are not well-suited to meeting the requirements of the proposed new standards.

For example, higher image rejection in front of or within the mixer 8 or 8a will be required because, with wider channels, the wanted and image channels will be closer together. Also, the bandwidths of the tracking filter of the stage 7 or of the high IF filter 16 will need to be wider in order to cope with the wider channel bandwidths. This makes the image rejection problem worse and also allows more potentially interfering signals to be passed in the case where lower bandwidth channels are to be received. Thus, the potential for intermodulation distortion is increased. Providing bandpass filters of variable bandwidth for the filters 12 and 16 causes substantial problems and is impractical for tracking filters in the circuit 7 of the tuner shown in Figure 1. Thus, tuners of the type shown in Figures 1 and 2 are unlikely to provide satisfactory performance for the proposed new standards.

GB 2 223 900 discloses a television tuner for reception of signals broadcast by satellite. The tuner has an input switch for selecting between signals having two orthogonal linear polarisations and in different frequency ranges. There is an image-tracking filter ahead of the mixer, which performs up-conversion to a high intermediate frequency. The local oscillator incorporates band-switching for receiving different frequency ranges.

US 4 162 451 and US 4 162 452 disclose a dual-standard television receiver for receiving channels in the UHF band and the VHF bands. Radio frequency switching is provided for selecting between high and low VHF bands. Separate front ends are provided for VHF and UHF reception in some of the disclosed examples. In one of the disclosed examples, switching between VHF and UHF bands is performed upstream of a common mixer, which is provided with two voltage-controlled oscillators for covering the different frequency ranges.

According to the invention, there is provided a modem tuner as defined in the appended claim 1.

Preferred embodiments of the invention are defined in the other appended claims.

The present invention and various of its embodiments thus overcome or reduce many of the disadvantages of known modem tuners. For example, data streams from different sources are isolated in the frequency domain so that there is no or much reduced possibility of cross-contamination and interference.

It is possible to provide a tuner which is compatible with wired and wireless systems and with proposed extensions in flexible channel bandwidths and modulation schemes. Thus, it is easier to provide a universal modem arrangement.

It is possible to avoid the need for external IF gain stages and AGC circuits so as to provide a simpler arrangement. Supplying signals to the demodulator at baseband reduces the performance requirements on analogue/digital conversion. For example, the analogue bandwidth may be reduced to one tenth of that in known arrangements.

The use of zero intermediate frequency techniques means that there are no image frequencies to be taken into account. Also, the tuner may be simplified by not requiring any tracking components and by providing only one frequency conversion stage.

The invention will be further described, by way of example, with reference to the accompanying drawings, in which:

Figure 1 is a block circuit diagram of a first known type of modem tuner;

Figure 2 is a block circuit diagram of a second known type of modem tuner;

Figure 3 is a block circuit diagram of a modem tuner constituting an embodiment of the present invention; and

Figure 4 is a block circuit diagram of an oscillator bandswitching arrangement of the tuner of Figure 3.

Like reference numerals refer to like parts throughout the drawings and those parts which have been described hereinbefore will not be described further.

The modem tuner shown in Figure 3 comprises a cable input 2 connected to a diplexer 20 which is connected to a first input of the tuner 6 and to an upstream modulator 21. The modulator 21 may, for example, be embodied by the same hardware as the circuit 14 and supplies signals via the diplexer 20 to the cable input 2 for distribution via a cable distribution system.

The satellite aerial is connected to an LNB 3, which converts the incoming signal to the L band and supplies this via a diplexer 22 to a second input of the receiver 6. The diplexer 22 also receives signals from the modulator 21 for distribution.

The inputs of the tuner 6 are connected to respective buffers 23 and 24 whose outputs are connected to a multiplexer 5. The output of the multiplexer 5 is connected to a mixer 8 which converts the incoming signals to zero intermediate frequency in-phase (I) and quadrature (Q) signal paths. A local oscillator 9 controlled by a PLL synthesiser 10 is connected to the mixer 8 via a bandswitch 25, which supplies in-phase and quadrature local oscillator signals to the mixer 8. The outputs of the mixer 8 are supplied via buffering 11 to a variable bandwidth low-pass filter 26, whose outputs are supplied to the circuit 14 for analogue/digital conversion, demodulation and forward error correction.

Each input of the tuner 6 receives signals in a different frequency range. For example, signals from the cable input are typically in a frequency band between 50 and 900MHz. The frequency-converted signals from the LNB 3 are in the L band 900MHz to 2.2GHz. Thus, by ensuring that the different input signal sources are in different frequency ranges, the possibility of interference between signals at the different inputs is eliminated or substantially reduced.

The individual input signals are independently buffered by the buffers 23 and 24 before being selected by the multiplexer 5. The filter 26 is embodied as a low-pass filter whose bandwidth can therefore easily be varied in order to cope with different channel widths for data streams. By converting the input signals to zero intermediate frequency, image problems are substantially avoided and the requirements of the analogue/digital conversion in the circuit 14 are greatly reduced by performing the conversion at baseband.

The bandswitch 25 is shown in more detail in Figure 4 and comprises a divide by two circuit 30 which divides the local oscillator signal (having a frequency of between 8 and 12GHz) by two. The output of the circuit 30 is supplied to a further divide by two circuit 31 and to a divide by 3 circuit 32, both of whose outputs are connected to respective inputs of a multiplexer (MUX) 33. Although single connections are shown, these are all duplicated to provide the quadrature signals to the mixer 8. The outputs of the circuits 31 and 32 are also supplied to respective inputs of a further multiplexer 34, whose output is connected to a chain of further divide by two circuits 35 to 39, each of whose outputs is connected to a respective input of the multiplexer 33.

The frequency ranges of the signals at the inputs of the multiplexer 33 are indicated in Figure 4. There are two frequency ranges at the outputs of each of the divide by two circuits 35 to 39 depending on whether the multiplexer 34 selects the output of the divide by two circuit 31 or the output of the divide by three circuit 32. The bandswitching arrangement is thus capable of supplying quadrature signals with a frequency range of 0.05 to 2.15GHz without any gaps in frequency coverage and using a local oscillator tuning range of 8 to 12GHz.

It is thus possible to provide a modem tuner of improved performance and reduced complexity and cost. Such a tuner is capable of receiving data streams of different standards, such as different channel bandwidths and different bit rates, from any of a variety of sources such as cable distribution systems, MMDS and LMDS. Tracking components can be eliminated and non-zero intermediate frequency bandwidth problems can be avoided.

CLAIMS:

1. A modem tuner of zero or near-zero intermediate frequency type for receiving signals modulated with digital data, comprising: a first input for signals in a first frequency range f_1 to f_2 ; a second input for signals in a second frequency range f_3 and f_4 , where $f_1 > f_2 > f_3 > f_4$; a mixer selectively connectable to one of the first and second inputs; a local oscillator having band switching for supplying to the mixer a local oscillator signal in any selected one of a plurality of local oscillator frequency ranges; and channel selective filtering located exclusively downstream of the mixer.
2. A tuner as claimed in claim 1 of zero intermediate frequency type.
3. A tuner as claimed in claim 1 or 2, in which the channel selective filtering has a variable bandwidth.
4. A tuner as claimed in any one of the preceding claims, in which the channel selective filtering comprises low pass filtering.
5. A tuner as claimed in any one of the preceding claims, in which the first frequency range is substantially within a first band from 50 to 900MHz.
6. A tuner as claimed in any one of the preceding claims, in which the second frequency ranges is substantially within a second band from 900MHz to 2.2GHz.
7. A tuner as claimed in any one of the preceding claims, in which the local oscillator frequency ranges comprise first and second local oscillator frequency ranges.
8. A tuner as claimed in claim 7, comprising a multiplexer for selectively connecting a signal input of the mixer to any one of the first and second inputs.
9. A tuner as claimed in claim 8, comprising first and second buffers connected between the first and second inputs, respectively, and the multiplexer.

10. A tuner as claimed in any one of the preceding claims, in which the mixer has in-phase and quadrature outputs.
11. A modem tuner substantially as hereinbefore described with reference to and as illustrated in Figures 3 and 4 of the accompanying drawings.



Application No: GB 0125125.5
Claims searched: 1 to 11

91

Examiner: John Donaldson
Date of search: 31 May 2002

Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.T): H3R(RMX)

Int Cl (Ed.7): H03D 7/00, 7/06, 7/08, 7/10, 7/12, 7/16

Other: Online: WPI, EPODOC, JAPIO

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	GB 2223900 A (VIDEO TECHNOLOGY), see page 9, line 8 to page 10, line 1	-
A	US 4162452 (ASH), see column 5, lines 18 to 55, column 17, lines 1 to 11	-
A	US 4162451 (ASH), see column 5, lines 18 to 55, column 17, lines 1 to 11	-

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.